



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of

Applicant: Kimikatsu SHOJI, et al.

Application No.: 09/377,740

Filed: August 20, 1999

For: SEMICONDUCTOR
INTEGRATED CIRCUIT
AND METHOD OF
MANUFACTURING THE
SAME

Attorney Docket: 32014-150502

Group Art Unit: 2811

Examiner: J. Carroll

December 8, 2000

AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

This is in response to the Office Action of September 25, 2000. Please amend the
above-identified application as follows:

In the Disclosure:

Please amend the disclosure as follows:

Page 1, line 14, before "IC", replace "the" with --an--.

line 24, delete "less".

page 2, line 5, delete "are formed,".

line 10, replace "[Customized Layer]" with --a "customized layer"--.

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line 12, replace "[Non-customized Layer]" with --a "non-customized layer"--.

line 17, replace "are not" with --have not been--.

¹⁸
line 28, replace "short TAT (TurnAround Time)" with --shorter turn around time

a 1 ("TAT")--.

line 24, between "The" and "dedicated", insert --layout of--.

line 25, replace "layout-designed" with --designed--.

page 3, line 9, replace "decreases in the standard" with --is decreased--.

line 10, delete "cell system or full custom system".

line 12, replace "made long" with --longer--.

line 17, replace "the" with --a--.

line 23, replace "an" with --a significant--.

line 24, delete "in no small numbers".

page 4, line 5, after "blocks", insert --and a gate array block, each of these functional

a 2 blocks--.

line 6, between "devices," and "comprising", insert --the method--.

line 8, replace "basic cell" with --gate array--.

lines 12-13, replace "basic cell" with --gate array--.

lines 14-15, replace "basic cell" with --gate array--.

line 18, delete "first".

page 5, line 5, between "chip" and "small", insert --having a--.

line 5, delete "in".

page 8, line 10, between "to" and "basic", insert --a--.

line 10, replace "re-laid" with --again laid--.